

Corelis JTAG Tools Help Maintain Test Engineer's Sanity While Increasing Throughput and Reducing Cost at Imperial Technology, Inc.



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CORELIS

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"A large, double-sided, very complex PC board assembly was being tested in a 'hot mock-up' and was consuming seven Test Engineer hours to test, troubleshoot, and repair. This required that a high level Test Engineer be dedicated to this testing function. The repetitious testing strained his sanity and added considerable cost to the process," said Bruce C. Johnson.

Bruce is the Quality Assurance Manager at Imperial Technology and is responsible for Inspection, Test, and Test Engineering. Imperial Technology, located in El Segundo, California, designs and manufactures state-of-the-art high speed, solid state disk drive memories.

In minimal time, Corelis provided a turn-key solution that included the ScanPlus boundary-scan test system and a custom test procedure.

"Corelis Engineers quickly became knowledgeable with our product and efficiently generated Test Vectors with excellent test coverage. Source inspection, which included inserting many faults, was performed at the Corelis facility and successfully accomplished in a speedy and cooperative manner. Test coverage was as quoted and fault locations

Boundary-Scan Without Boundaries™



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were well displayed. On-site training was also provided to our Test Engineer.”

“Our board was not particularly designed with boundary-scan in mind; however, test results were most gratifying,” said Mr. Johnson. “All accessible failing nodes were easily identified leading to faulty parts or the failure cause. The majority of boards which passed the boundary test also passed functional testing in the hot mock-up. We are now using technicians to perform the testing and fault location using the Corelis solution, freeing the Test Engineer to perform more demanding tasks.”

“We were so pleased with the results,” added Johnson, “that we proceeded with Corelis to perform test vectors generation for two additional boards which incorporated BGA parts and a multitude of FPGA parts. Source acceptance testing located faults which the Designing Engineer had unsuccessfully devoted weeks to locate. Boards which tested GOOD using boundary-scan performed 100% in subsequent system functional tests. The Corelis boundary-scan solution has saved a substantial investment in Test Engineer and test fixturing resources. These valuable resources may now be utilized in more productive and satisfying applications.”

Johnson was so pleased with the effectiveness of boundary-scan and Corelis boundary-scan test tools that he is incorporating boundary-scan testability into all new product designs at Imperial Technology.

About Corelis

Corelis, Inc., a subsidiary of Electronic Warfare Associates, Inc., offers bus analysis tools, embedded test tools, and the industry’s broadest line of JTAG/boundary-scan software and hardware products combining exceptional ease-of-use with advanced technical innovation and unmatched customer service. Corelis’ development and test tools are used by companies such as Agilent, Dell, IBM, Intel, Microsoft, Lockheed Martin, Rockwell Collins, Hewlett-Packard, Motorola, Qualcomm, Nokia, Panasonic, TI, Ford, Broadcom, Ericsson, and many others. Corelis products are found globally in every industry developing or manufacturing electronic products.